



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichio YUDASAKA et al.

Group Art Unit: 2826

Application No.: 10/717,503

Examiner: T. QUACH

Filed: November 21, 2003

Docket No.: 117849

For: TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC
INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR

AMENDMENT UNDER 37 C.F.R. §1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the April 6, 2005 Office Action, and in consideration of the July 6, 2005
personal interview with the Examiner, please consider the following:

Amendments to the Claims as reflected in the listing of claims; and

Remarks.